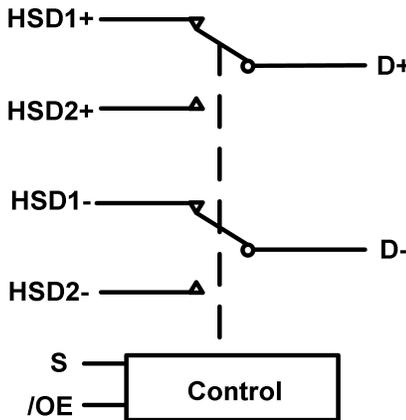


GT4222

High-Speed USB2.0 (480Mbps) DPDT Analog Switch

1 Features	2 Application
<ul style="list-style-type: none"> - Supply Range: 1.8 V to 5.5 V - Rail-to-Rail Signal Range - On-Resistance Matching: 0.05 Ω (TYP) - ON-Resistance: 4.5 Ω (TYP) at 4.5 V - ON-Resistance Flatness: 0.9 Ω (TYP) at 4.5 V - High Off Isolation: -32 dB at 250 MHz - -41 dB (250 MHz) Crosstalk Rejection Reduces - Break-Before-Make Switching - -3 dB Bandwidth: 720 MHz - Operation Temperature Range: -40° C to +125° C 	<ul style="list-style-type: none"> - Cell Phones - PDAs - Portable Instrumentation - Differential Signal Data Routings - USB 2.0 Signal Routing

3 Description	Circuit Diagram
<p>The GT4222 is a high bandwidth, fast double-pole double-throw (DPDT) analog switch. Its wide bandwidth and low bit-to-bit skew allow it to pass high-speed differential signals with good signal integrity. Each switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. Its high channel-to-channel crosstalk rejection results in minimal noise interference.</p>	

4 Device Summary, Pin and Packages

Table 4-1. Device Summary⁽¹⁾

Serial Name	Part Name	Package	Body Size (Nom)	Marking ⁽²⁾	MSL ⁽³⁾	Package Qty
GT4222	GT4222MA	MSOP10	3.00mm*3.00mm*1.10mm	GT4222 XXXXXXX	3	Tape and Reel,4000
	GT4222QA	QFN1.4*1.8-10L	1.40mm*1.80mm*0.55mm	4222 XXXX	3	Tape and Reel,4000

(1) For all available packages, please contact product sales.

(2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.

(3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

(4) "XXXXX" in Marking will be appeared as the batch code.

4 Device Summary, Pin and Packages(Continued)

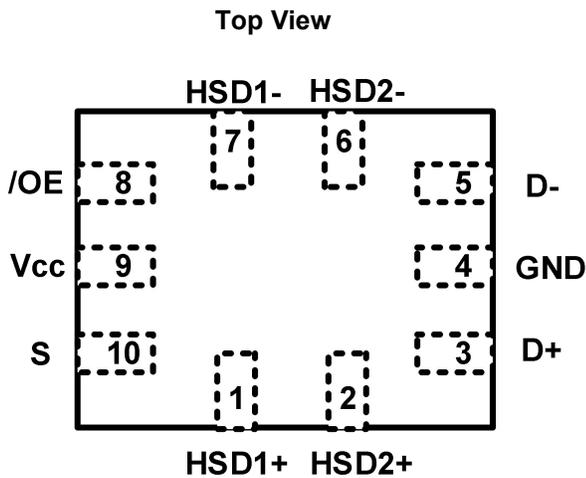


Fig.4-1.GT4222: QA (QFN1.4*1.8-10L) Package

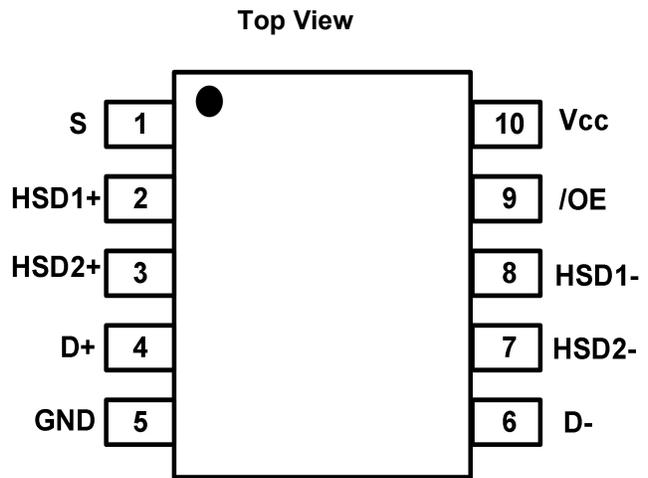


Fig.4-2.GT4222: MA (MSOP10) Package

Table 4-2 Pin Description

Name	Pin Number		Description
	QA	MA	
Vcc	9	10	Positive Power Supply
S	10	1	Select Input
D+,D-	3,5	4,6	Data Ports
HSD1+,HSD1-	1,7	2,7	Data Ports 1
HSD2+,HSD2-	2,6	3,8	Data Ports 2
/OE	8	9	Output Enable
GND	4	5	Ground

Table 4-3 Logic Function Table

/OE	S	HSD1+,HSD1-	HSD2+,HSD2-
0	0	ON	OFF
0	1	OFF	ON
1	X	OFF	OFF

5 Voltage, Temperature, ESD and Thermal Ratings

5.1 Absolute Maximum Ratings⁽¹⁾

Parameters		Min.	Max.	Unit
V _{CC}	Supply Voltage Range	-0.3	6	V
V _{IS}	Analog, Digital Voltage Range ⁽²⁾	-0.3	(V _{CC})+0.3	V
I _{IN}	Continuous Current HSDn or Dn	-100	+100	mA
I _{peak}	Peak Current HSDn or Dn	-150	+150	mA
T _J	Junction Temperature		150	°C
T _{stg}	Storage Temperature	-65	150	°C

(1) Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

5.2 ESD Ratings

ESD		Value	Unit	
V(ESD)	Electrostatic Discharge	Human-Body Model (HBM) ⁽¹⁾	4.5K	V
		Charged-Device Model (CDM) ⁽²⁾	2K	V

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

5.3 Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)

Symbol	Parameter	Min	Max	Unit
V _{CC}	Supply Voltage	1.8	5.5	V
T _A	Operating Temperature	-40	125	°C

(1) All unused digital inputs of the device must be held at V_{CC} or GND to ensure proper device operation.

5.4 Thermal Information

Package Type	θ_{JA}	θ_{JC}	Unit
MSOP10	180.7	66.2	°C/W
QFN1.4*1.8-10L	120	46	°C/W

6 Electrical Specifications

6.1 DC Electrical Characteristics

V_{CC}=1.8V to 5.5 V, GND=0V, FULL=-40°C to +125°C. Typical values are at T_A=+25°C. (unless otherwise noted)

Parameter	Symbol	Conditions	V _{CC}	Temp	Min	Typ	Max	Units
Analog Switch								
On-Resistance	R _{ON}	V _{IS} =0V to 0.4V, I _D =10mA, See Fig.7-1.	4.5V	+25°C		4.5	5	Ω
				Full			7	
			3V	+25°C		6.5	7	Ω
				Full			10	
On-Resistance Flatness	R _{FLAT}	V _{IS} =0V to 1.0V, I _D =10mA, See Fig.7-1.	4.5V	+25°C		0.9	1.2	Ω
				Full			1.5	
			3V	+25°C		3.2	3.5	Ω
				Full			4.5	
On-Resistance Matching	ΔR _{ON}	V _{IS} =0V to 0.4V, I _D =10mA, See Fig.7-1.	4.5V	+25°C		0.05	0.1	Ω
				Full			0.15	
			3V	+25°C		0.05	0.1	Ω
				Full			0.15	
Increase In I _{CC} per Control Voltage	I _{CC} T	V _S or V _{IOE} =2.6 V	4.3V	Full			20	μA
Source Off Leakage Current	I _{HSD2(OFF)} I _{HSD1(OFF)}	V _{IS} =5V/ 0.5V, V _D =0.5V/ 5V	5.5V	Full			1	μA
Channel On Leakage Current	I _{HSD2(ON)} I _{HSD1(ON)}	V _{IS} =5V/ 0.5V, V _D =floating	5.5V	Full			1	μA
Digital Control Inputs								
Input High Voltage	V _{IH}		3.3V	Full	1.7			V
Input Low Voltage	V _{IL}		3.3V	Full			0.5	V
Input Leakage current	I _{IN}	V _S , V _{IOE} =0V or V _{CC}	5.5V	Full			1	μA
Power Requirements								
Power Supply Range	V _{CC}			Full	1.8		5.5	V
Quiescent Supply Current	I _{CC}	V _S , V _{IOE} =0V or V _{CC}	5.5V	Full			1	μA

6 Electrical Specifications(Continued)

6.2 Switch And AC Characteristics

V_{CC}=1.8V to 5.5 V, GND=0V, FULL=-40°C to +125°C. Typical values are at T_A=+25°C. (unless otherwise noted)

Parameter	Symbol	Conditions	V _{CC}	Temp	Min	Typ	Max	Units
Turn-On Time	t _{ON}	V _{IS} =0.8V, R _L =50Ω, C _L =10pF, See Fig.7-2.	5V	+25°C		12		ns
			3.3V	+25°C		14		ns
Turn-Off Time	t _{OFF}	V _{IS} =0.8V, R _L =50Ω, C _L =10pF, See Fig.7-2.	5V	+25°C		6		ns
			3.3V	+25°C		8		ns
Break-Before-Make Delay	t _{BBM}	V _{IS} =0.8V, R _L =50Ω, C _L =10pF, See Fig.7-3.	5V	+25°C		5		ns
			3.3V	+25°C		4		ns
-3 dB Bandwidth	BW	Signal=0dBm, R _L =50Ω C _L =5pF, See Fig.7-6.	5V	+25°C		720		MHz
			3.3V	+25°C		700		MHz
Off-Isolation	OIRR	Signal=0dBm, R _L =50Ω, f=250MHz, See Fig.7-4.	5V	+25°C		-32		dB
			3.3V	+25°C		-32		dB
Non-Adjacent Channel Crosstalk	XTALK	Signal=0dBm, R _L =50Ω, f=250MHz, See Fig.7-5.	5V	+25°C		-41		dB
			3.3V	+25°C		-41		dB
Channel-to-Channel Skew	t _{SKEW}	R _L =50Ω, C _L =10pF	5V	+25°C		0.1		ns
		R _L =50Ω, C _L =10pF	3.3V	+25°C		0.1		ns
Charge Injection	Q	R _G =0Ω, C _L =1nF, See Fig.7-7.	5V	+25°C		6		pC
		R _G =0Ω, C _L =1nF, See Fig.7-7.	3.3V	+25°C		4		pC
On Capacitance	C _{ON}		5V	+25°C		6		pF
			3.3V	+25°C		6		pF

7 Measurement Information

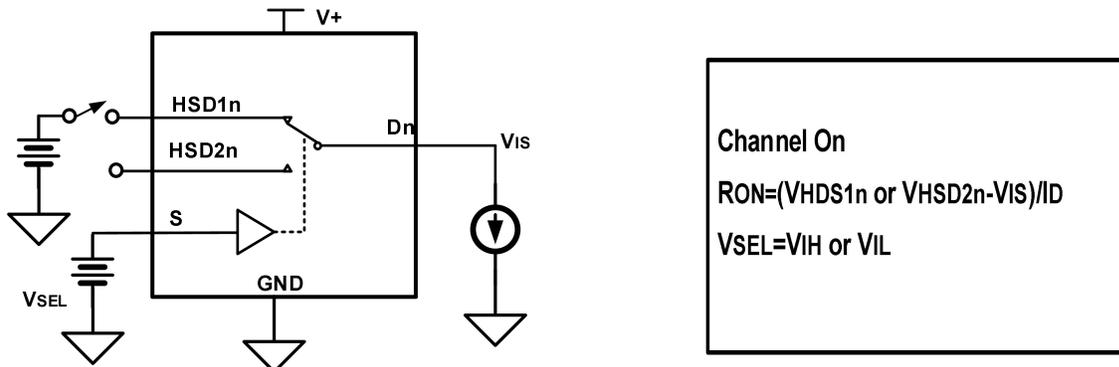


Fig.7-1. ON-State Resistance (R_{ON})

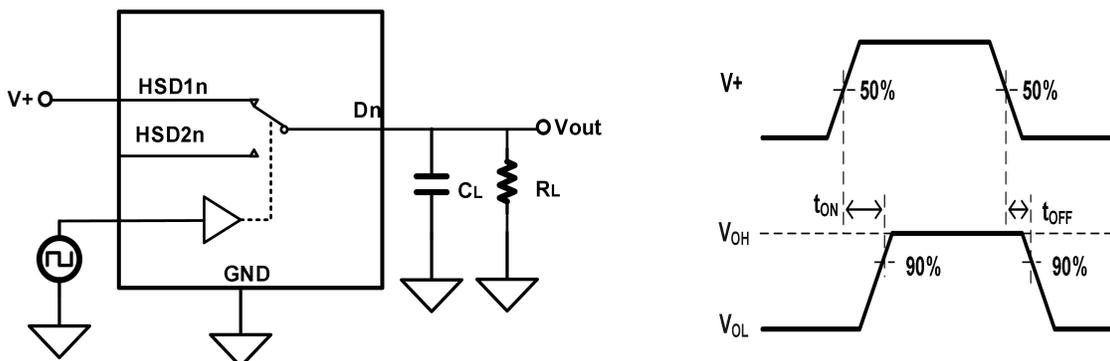


Fig.7-2. Turn-On (t_{ON}) and Turn-Off Time (t_{OFF})

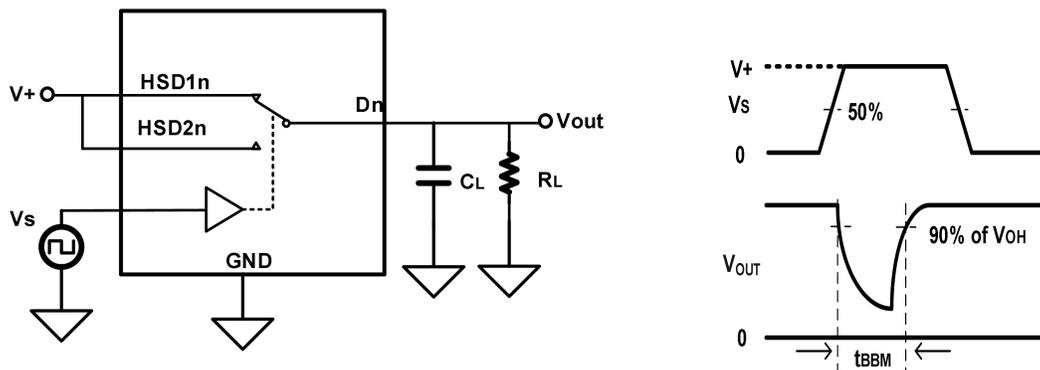


Fig.7-3. Break-Before-Make Time Delay (t_{BBM})

7 Measurement Information(Continued)

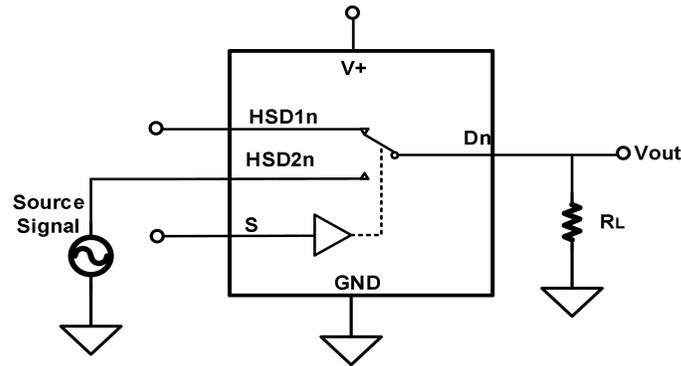
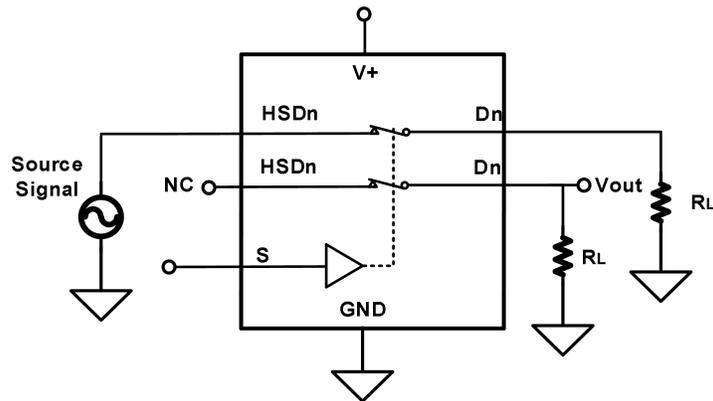


Fig.7-4. Off Isolation(OIRR)



Channel To Channel Crosstalk = $-20 \times \log (V_{HSDn}/V_{OUT})$

Fig.7-5. Channel-to-Channel Crosstalk

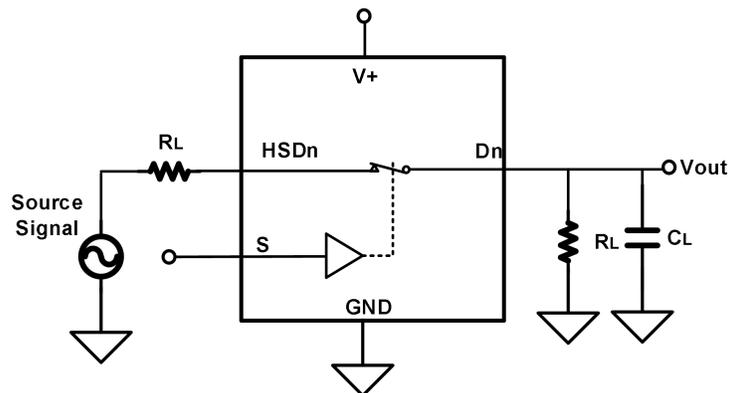


Fig.7-6. -3dB Bandwidth

7 Measurement Information(Continued)

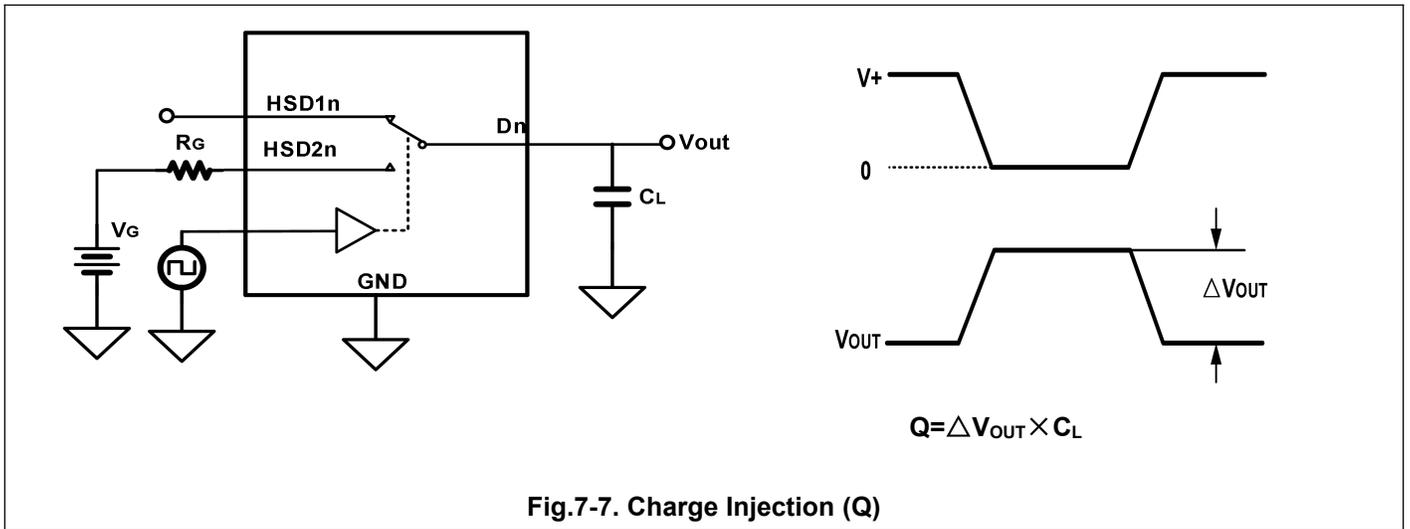


Fig.7-7. Charge Injection (Q)

8 Applications Note

There are many USB applications in which the USB hubs or controllers have a limited number of USB I/Os. The GT4222 solution can effectively expand the limited USB I/Os by switching between multiple USB buses in order to interface them to a single USB hub or controller. GT4222 can also be used to connect a single controller to two USB connectors or controllers.

Design requirements of the USB 1.0, 1.1, and 2.0 standards should be followed. It is recommended that the digital control pins S and /OE be pulled up to Vcc or down to GND to avoid undesired switch positions that could result from the floating pin.

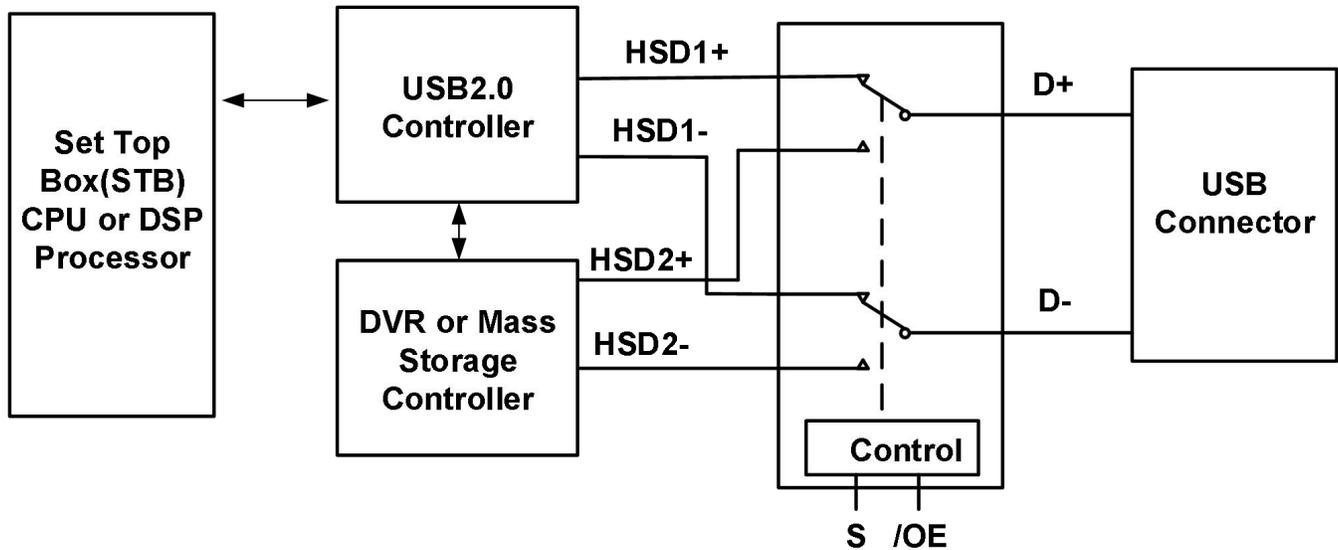
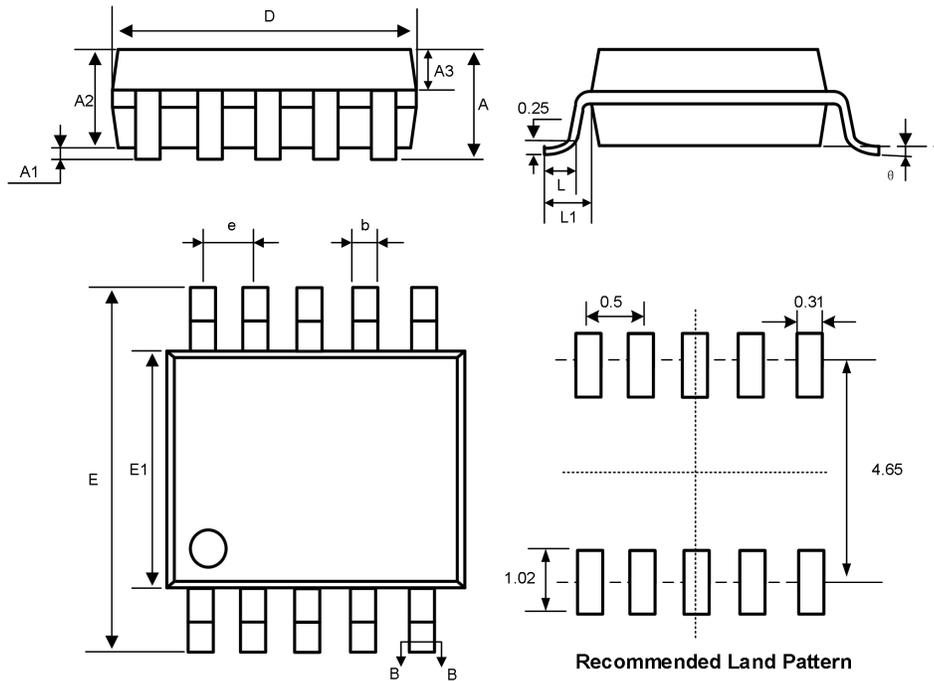


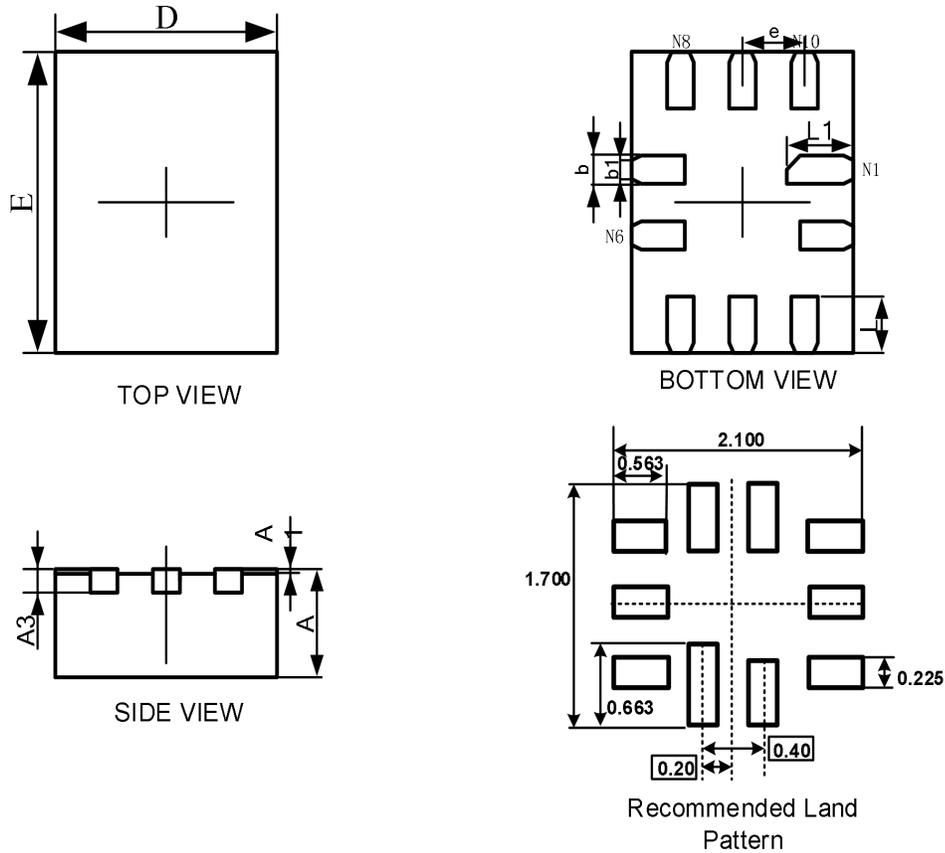
Fig.8-1. Application Diagram

9 Package Outline Dimension

MSOP10

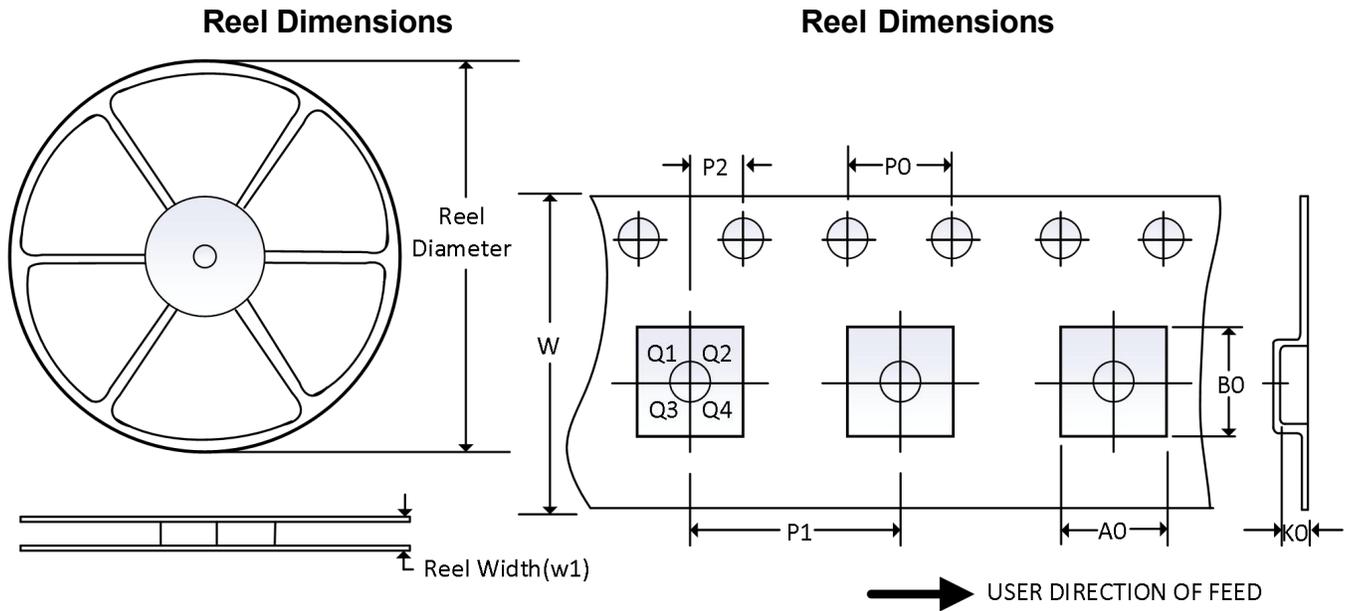


Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	—	—	1.10	—	—	0.043
A1	0.05	—	0.15	0.002	—	0.006
A2	1.30	1.40	1.50	0.051	0.055	0.059
A3	0.75	0.85	0.95	0.030	0.033	0.037
D	2.90	3.00	3.10	0.114	0.118	0.122
E	4.70	4.90	5.10	0.185	0.193	0.201
E1	2.90	3.00	3.10	0.114	0.118	0.122
e	0.50BSC			0.02BSC		
L	0.40	—	0.70	0.016	—	0.028
L1	0.95REF			0.037REF		
θ	0	—	8°	0	—	8°

9 Package Outline Dimension(Continued)
QFN1.4*1.8-10L


Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min	Max	Min	Max
A	0.500	0.600	0.020	0.024
A1	0.000	0.050	0.000	0.002
A3	0.152REF		0.006REF	
D	1.350	1.450	0.053	0.057
E	1.750	1.850	0.069	0.073
D1	—	—	—	—
E1	—	—	—	—
k	—		—	
b	0.150	0.250	0.006	0.010
b1	0.100	0.200	0.004	0.008
e	0.400TYP		0.016TYP	
L	0.350	0.450	0.014	0.018
L1	0.450	0.550	0.018	0.022

10 Tape and Reel Information



NOTE: The picture is only for reference. Please make the object as the standard.

Key Parameter List of Tape and Reel

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
MSOP10	13"	12.4	5.2	3.3	1.2	4.0	8.0	2.0	12.0	Q1
QFN1.4*1.8-10L	7"	9	1.6	2	0.85	4.0	4.0	2.0	8.0	Q1

- NOTE:
1. All dimensions are nominal.
 2. Plastic or metal protrusions of 0.15mm maximum per side are not included.